

**In the Specification**

Please replace the "Related Applications" paragraph as amended in the response filed on July 27, 2000 with the following marked up paragraph:

--The present application is a continuation of U.S. Patent Application serial number 08/478,413 filed on June 7, 1995, now abandoned, which is a divisional of U.S. Patent Application serial number 07/954,970, now U.S. Patent number 6,304,891, filed on September 30, 1992.--

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8/25/04

Please replace the paragraph on page 8 starting at line 2 with the following marked up paragraph:

-- This application is related to the following co-pending patent applications filed concurrently herewith:

A patent application entitled INTERTASK BUFFER AND CONNECTIONS, whose inventors are A. Philip Sohn and Eric Anderson, which has been assigned Serial No. 07/954,902, now U.S. Patent number 5,848,295.

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A patent application entitled A METHOD AND MEANS FOR PROVIDING MULTIPLE CLIENTS SIMULTANEOUS ACCESS TO A SOUND DATA STREAM, whose inventors are Eric Anderson and Hugh Svendsen, which has been assigned Serial No. 07/954,873, now U.S. Patent number 5,384,890.

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A patent application entitled APPARATUS AND METHOD FOR HANDLING FRAME OVERRUNS IN A DIGITAL SIGNAL PROCESSING SYSTEM, whose inventors are Eric Anderson and Hugh Svendsen, which has been assigned Serial No. 07/954,758, now U.S. Patent number 5,388,261.

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A patent application entitled APPARATUS AND METHOD FOR ALLOCATING PROCESSING TIME IN A FRAME-BASED COMPUTER SYSTEM, whose inventors are Eric Anderson and A. Philip Sohn, which has been assigned Serial No. 07/954,338, now U.S. Patent number 5,628,013.

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A patent application entitled TASK AND MODULE ORGANIZATION FOR PROCESSOR EXECUTION, whose inventors are Eric Anderson and Hugh B. Svendsen, which has been assigned Serial No. 07/954,988, now U.S. Patent number 5,448,735--

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Please replace the paragraph on page 17, starting at line 3 with the following marked up paragraph:

--Each DSP device, such as 602, maintains two independent task lists 610 and 630 which are used for keeping track of tasks currently running in the DSP operating system. One task list 610 is known as the "real-time" task list and is a set of routines which need to be operated upon at regular intervals. Each task, such as 611 through 614, in real-time task list 610 is executed only once during regular intervals so that the client requiring the services of each DSP task in task list 610 is serviced within a specific interval of time. A technique known as guaranteed processing bandwidth (GPB) is utilized to ensure that the tasks in real-time task list 610 do not exceed the maximum length of time in which real-time tasks may be executed. GPB is discussed in the co-pending application entitled "Apparatus and Method for Allocating Processing Time in a Frame-based Computer System" which has been assigned Serial No. 07/954,338 ~~and which is attached hereto as Appendix A~~, now U.S. Patent number 5,628,013, and which is incorporated herein by reference.--

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Please replace the paragraph on page 26, starting at line 8 with the following marked up paragraph:

--By using the average frames used and the average available timeshare processing per frame, the frequency in which a new timeshare task will be executed can be computed as follows:

current timeshare load = average timeshare • average frames used

proposed timeshare load = current timeshare load + GPB estimate of task

computed frames used = proposed timeshare load/average timeshare

(the GPB estimate of the task is described in the co-pending application entitled "Apparatus and Method for Allocating Processing Time in a Frame-based Computer System" which has been assigned Serial No. 07/954,338 ~~and is attached hereto as Appendix A~~), now U.S. Patent number 5,628,013, and which is incorporated herein by reference.--

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